

SS24UM

2.0A Low VF Surface Mount Schottky Barrier Rectifiers -40V

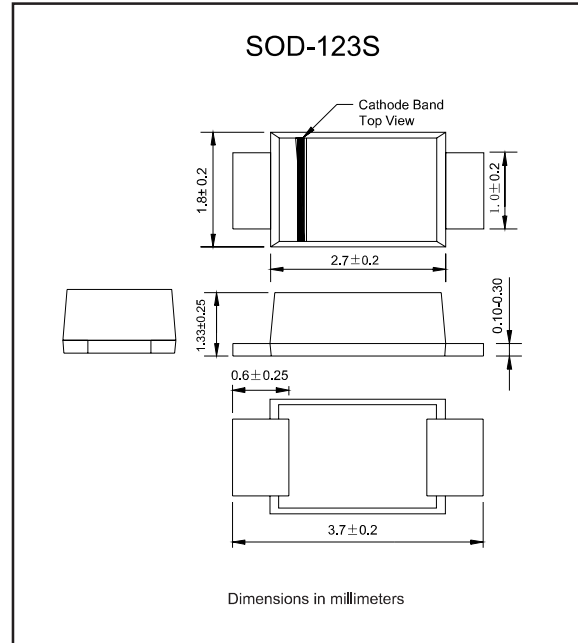
Features

- ◆ The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- ◆ Metal silicon junction, majority carrier conduction
- ◆ Low power loss, high efficiency
- ◆ High forward surge current capability
- ◆ High temperature soldering guaranteed:
250°C/10 seconds, 0.375" (9.5mm) lead length,
5 lbs. (2.3kg) tension
- ◆ Compliant to RoHS Directive 2011/65/EU
- ◆ Compliant to Halogen-free

Mechanical data

- ◆ **Case**: JEDEC SOD-123 molded plastic body
- ◆ **Terminals**: Plated axial leads, solderable per MIL-STD-750, Method 2026
- ◆ **Polarity**: Color band denotes cathode end
- ◆ **Mounting Position**: Any

Package outline



Maximum ratings and Electrical Characteristics (AT $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I_O			2.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I_{FSM}			40	A
Reverse current	$V_R = V_{RRM} \quad T_A = 25^\circ\text{C}$	I_R			1.0	mA
	$V_R = V_{RRM} \quad T_A = 100^\circ\text{C}$				20	
Thermal resistance	Junction to ambient NOTE 1	$R_{\theta JA}$		92		$^\circ\text{C/W}$
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C_J		130		pF
Storage temperature		T_{STG}	-65		+150	$^\circ\text{C}$

Note: 1.P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas

SYMBOLS	V_{RRM}^{*1} (V)	V_{RMS}^{*2} (V)	V_R^{*3} (V)	V_F^{*4} (V)	Operating temperature T_J , ($^\circ\text{C}$)
SS24UM	40	28	40	0.40	-55 to +125

*1 Repetitive peak reverse voltage

*2 RMS voltage

*3 Continuous reverse voltage

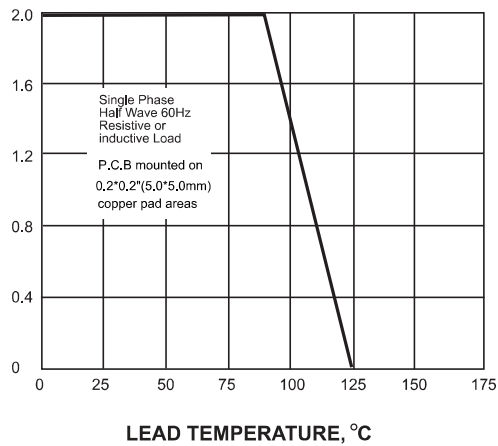
*4 Maximum forward voltage @ $I_F=2.0\text{A}$

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Rating and characteristic curves

AVERAGE FORWARD RECTIFIED CURRENT,
AMPERES

FIG. 1- FORWARD CURRENT DERATING CURVE



PEAK FORWARD SURGE CURRENT,
AMPERES

FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

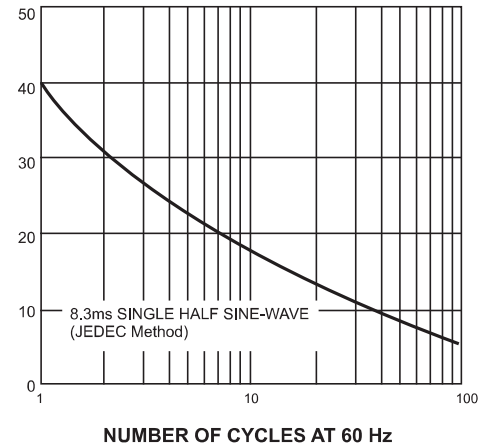


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

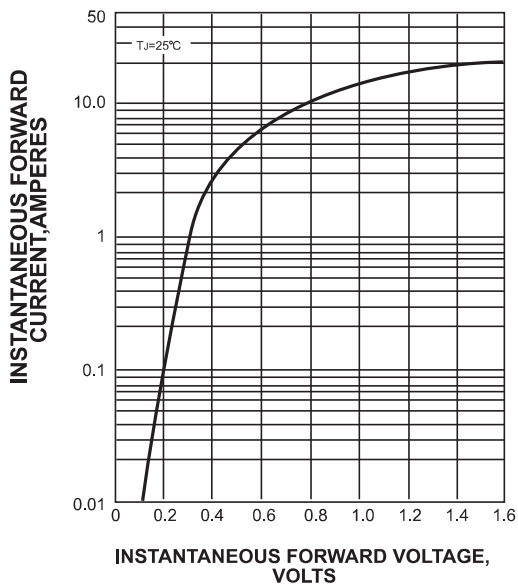


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

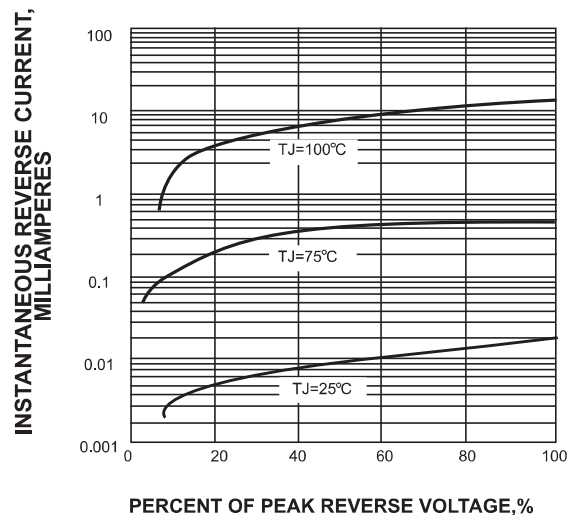


FIG. 5-TYPICAL JUNCTION CAPACITANCE

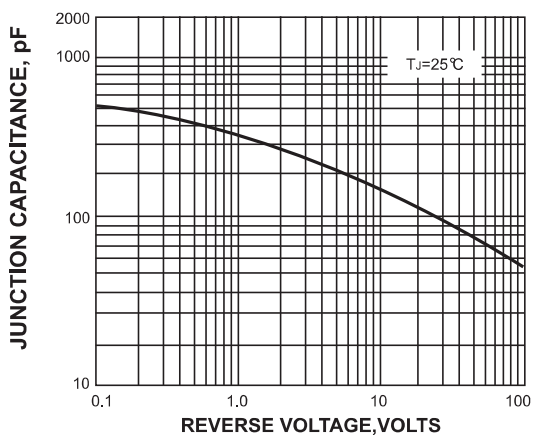
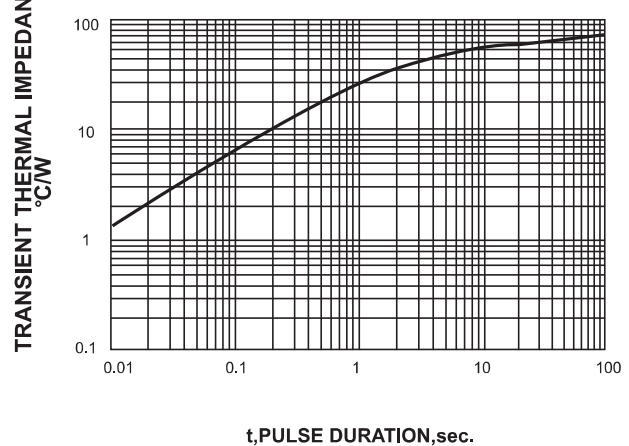
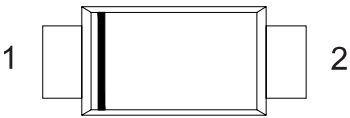


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE



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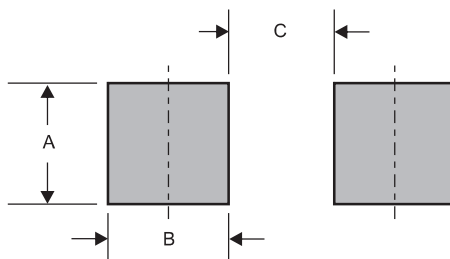
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

Marking

Type number	Marking code
SS24UM	24U

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-123S	0.075 (1.90)	0.055 (1.40)	0.075 (1.90)